

Fig. 1

Fig. 2

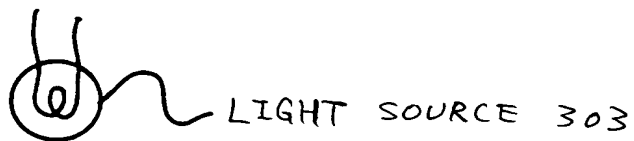
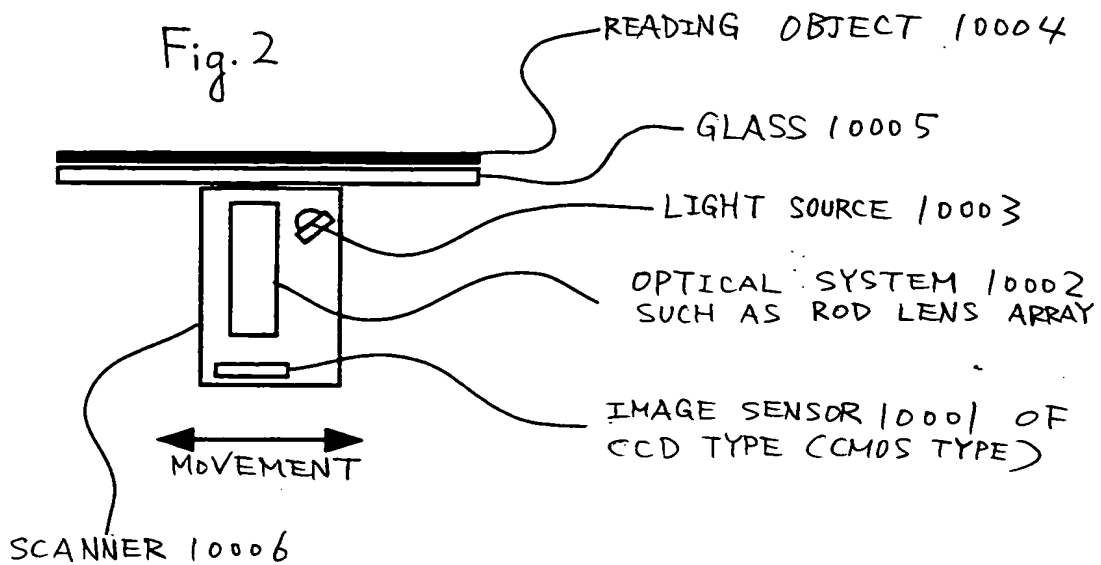


Fig. 3

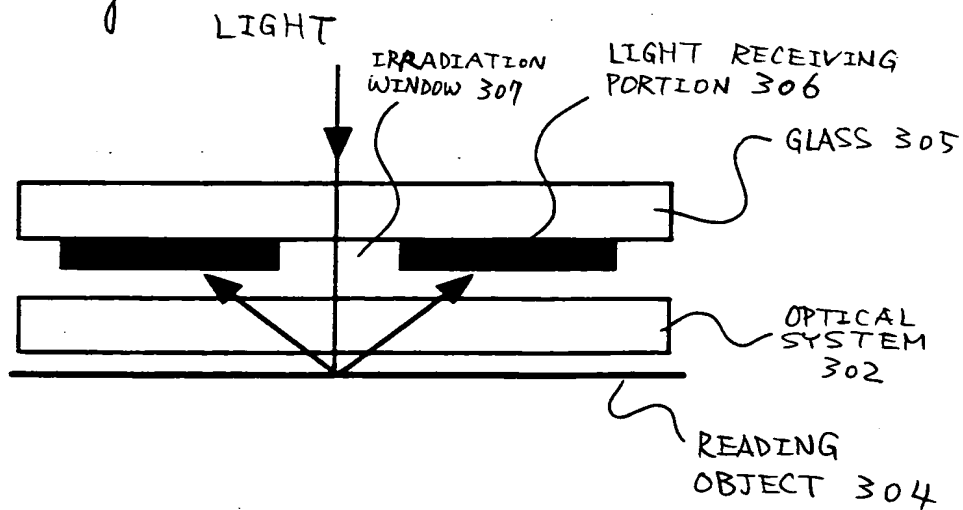


FIG. 4A

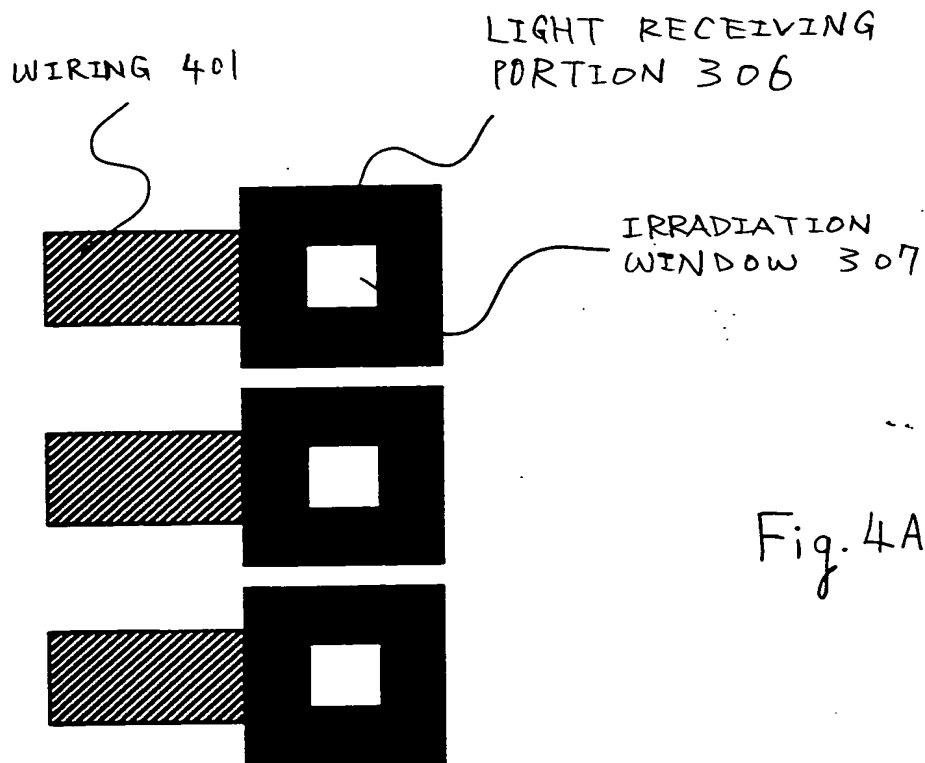


Fig. 4A

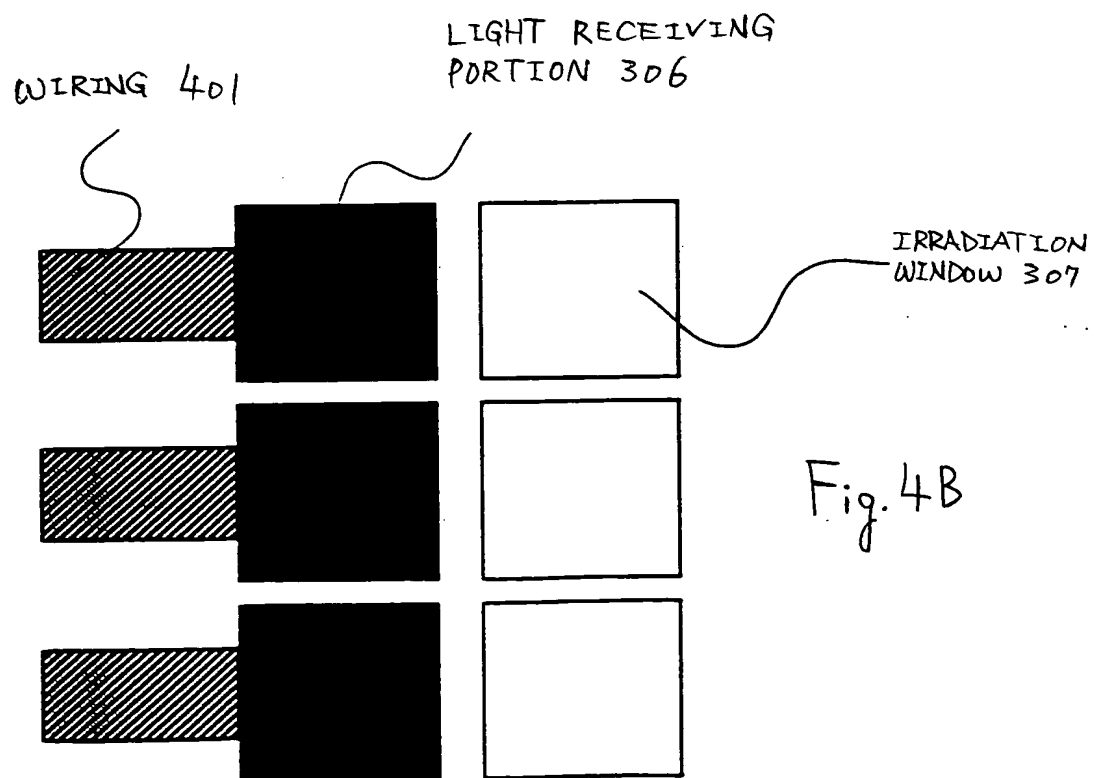
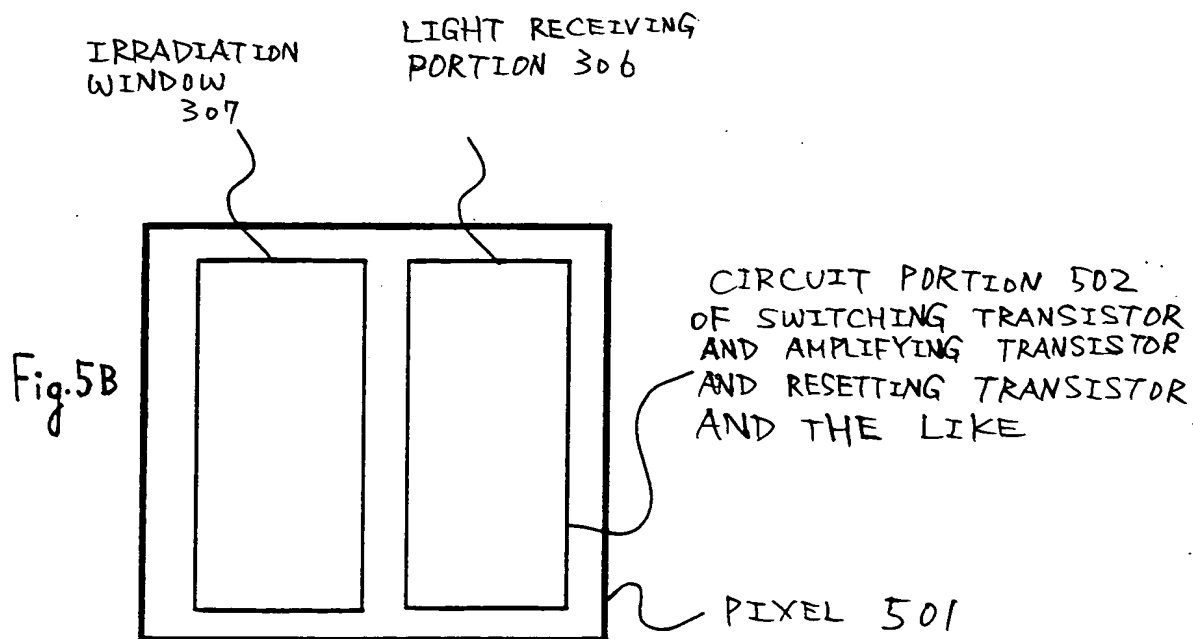
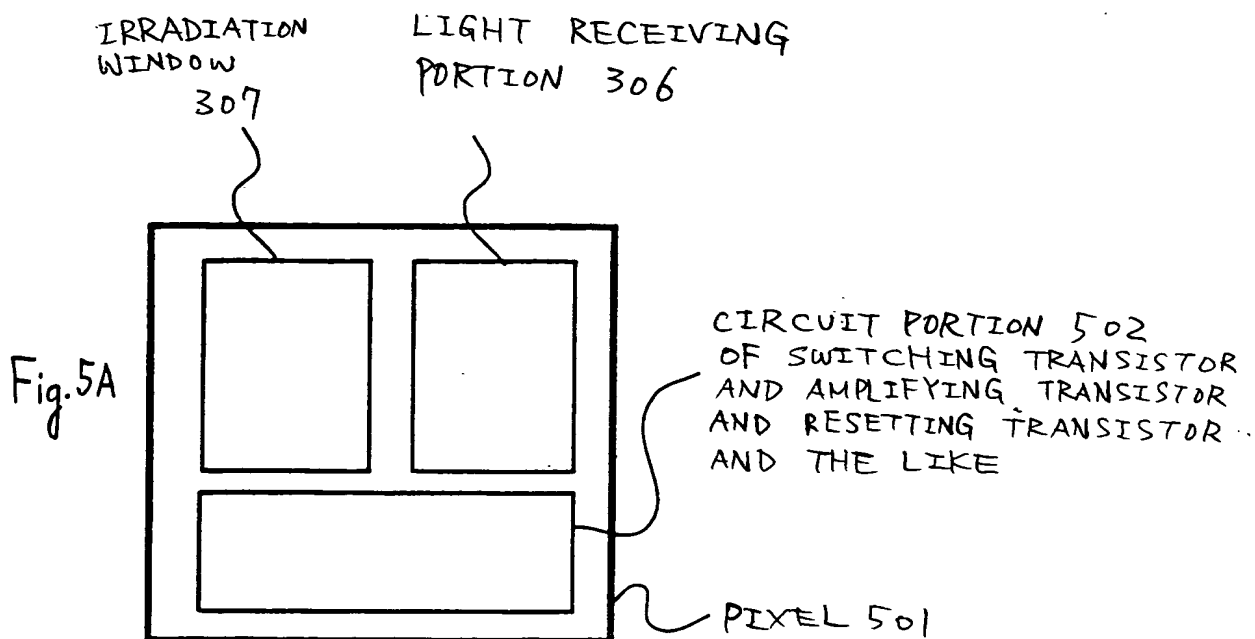


Fig. 4B



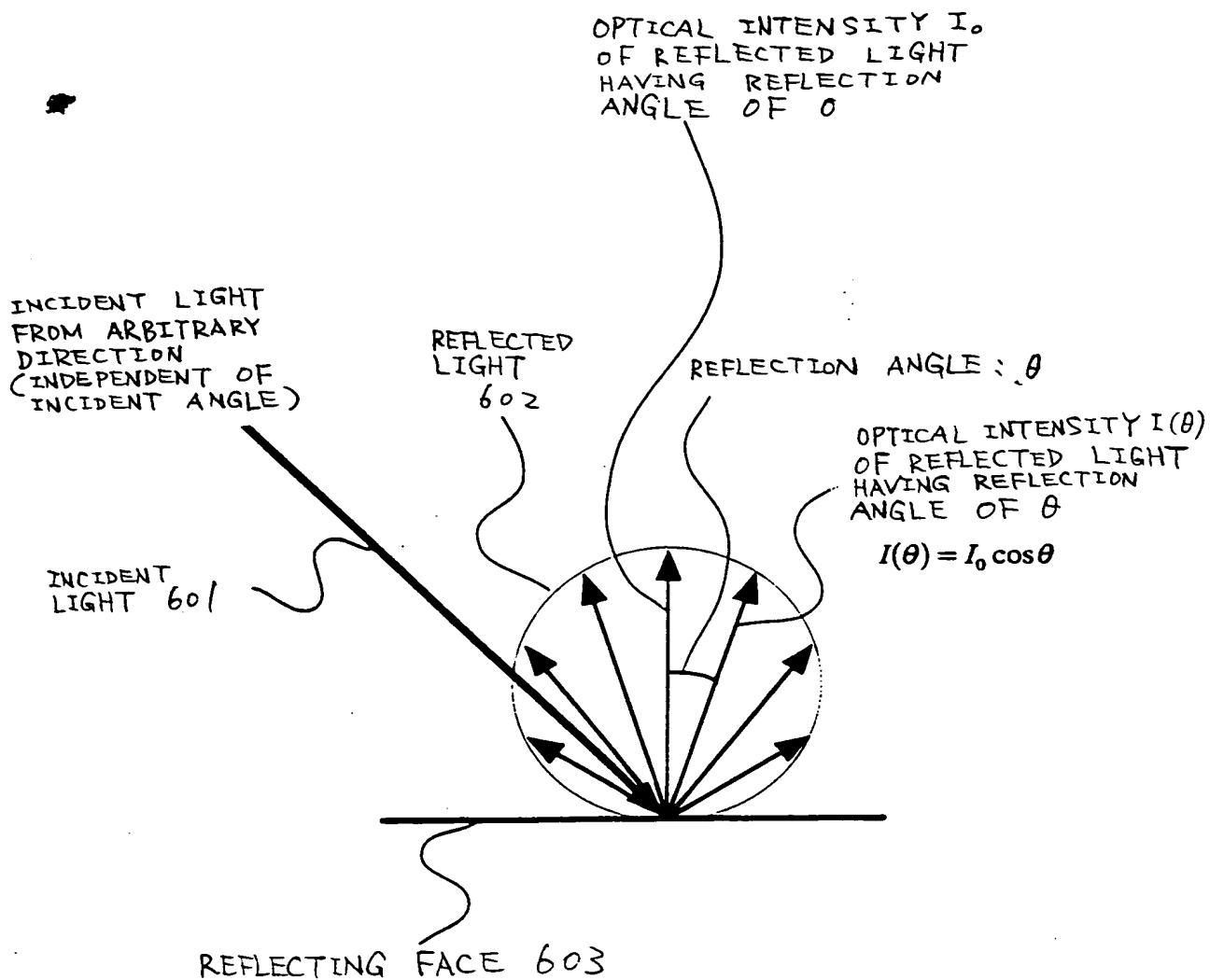


Fig. 6

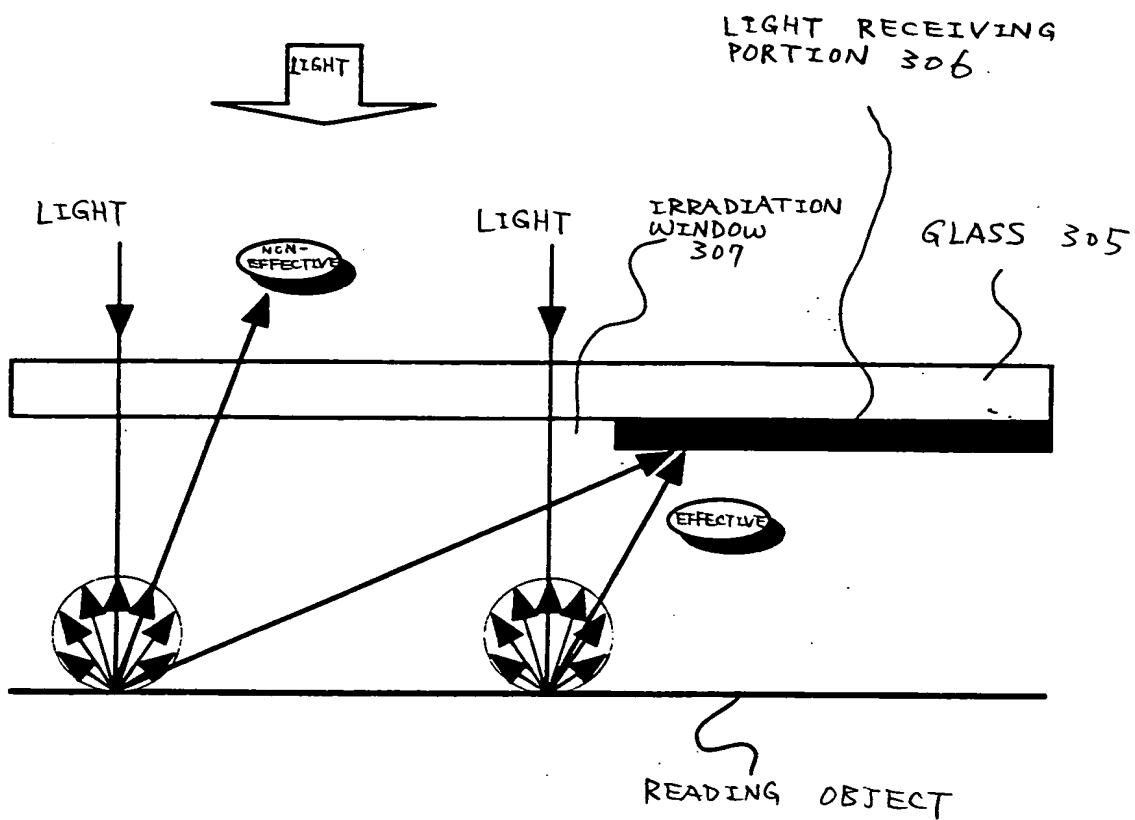


Fig-7

LIGHT RECEIVING PORTION 806

IRRADIATION  
WINDOW 807

A

A'

SECTION  
LINE 801

Fig. 8

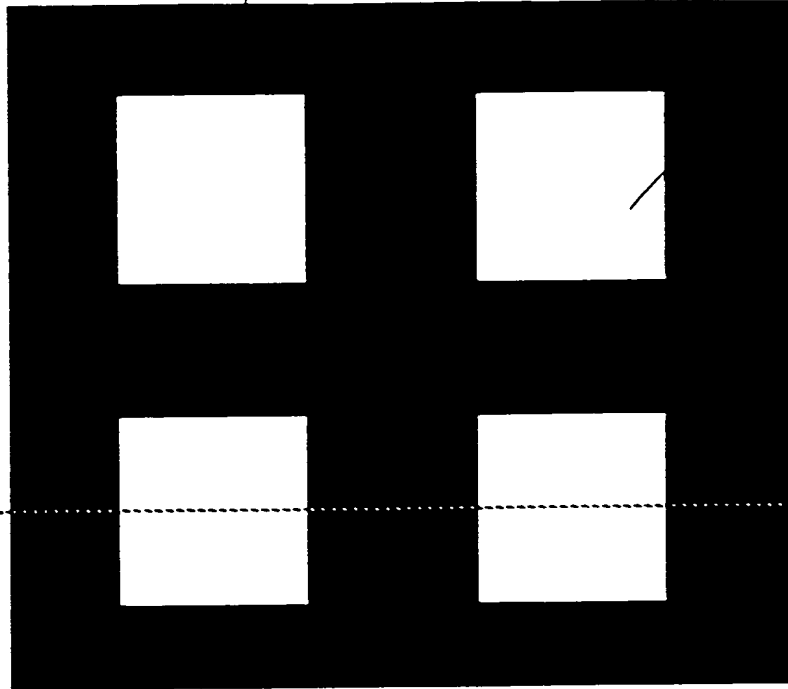
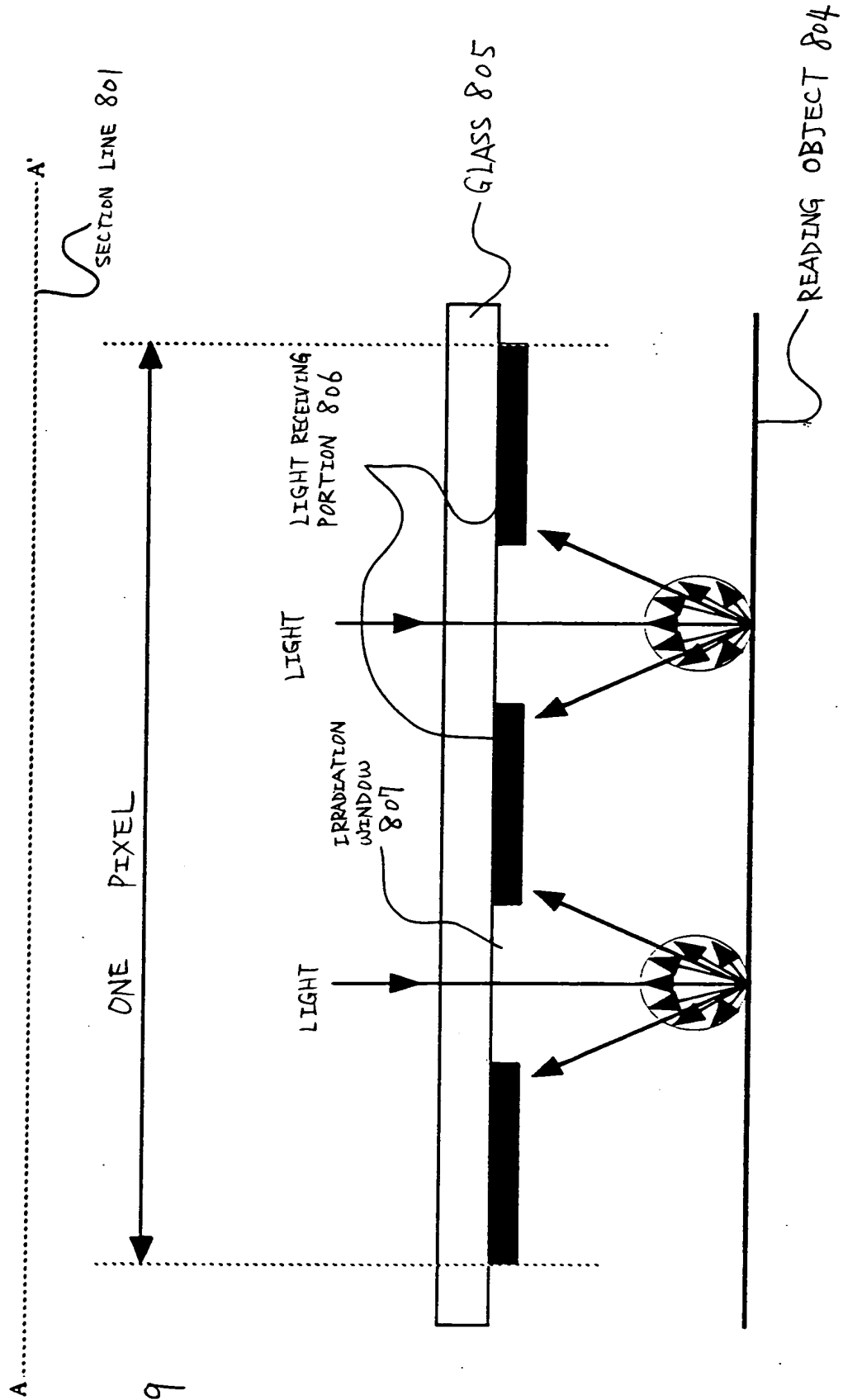


Fig. 9





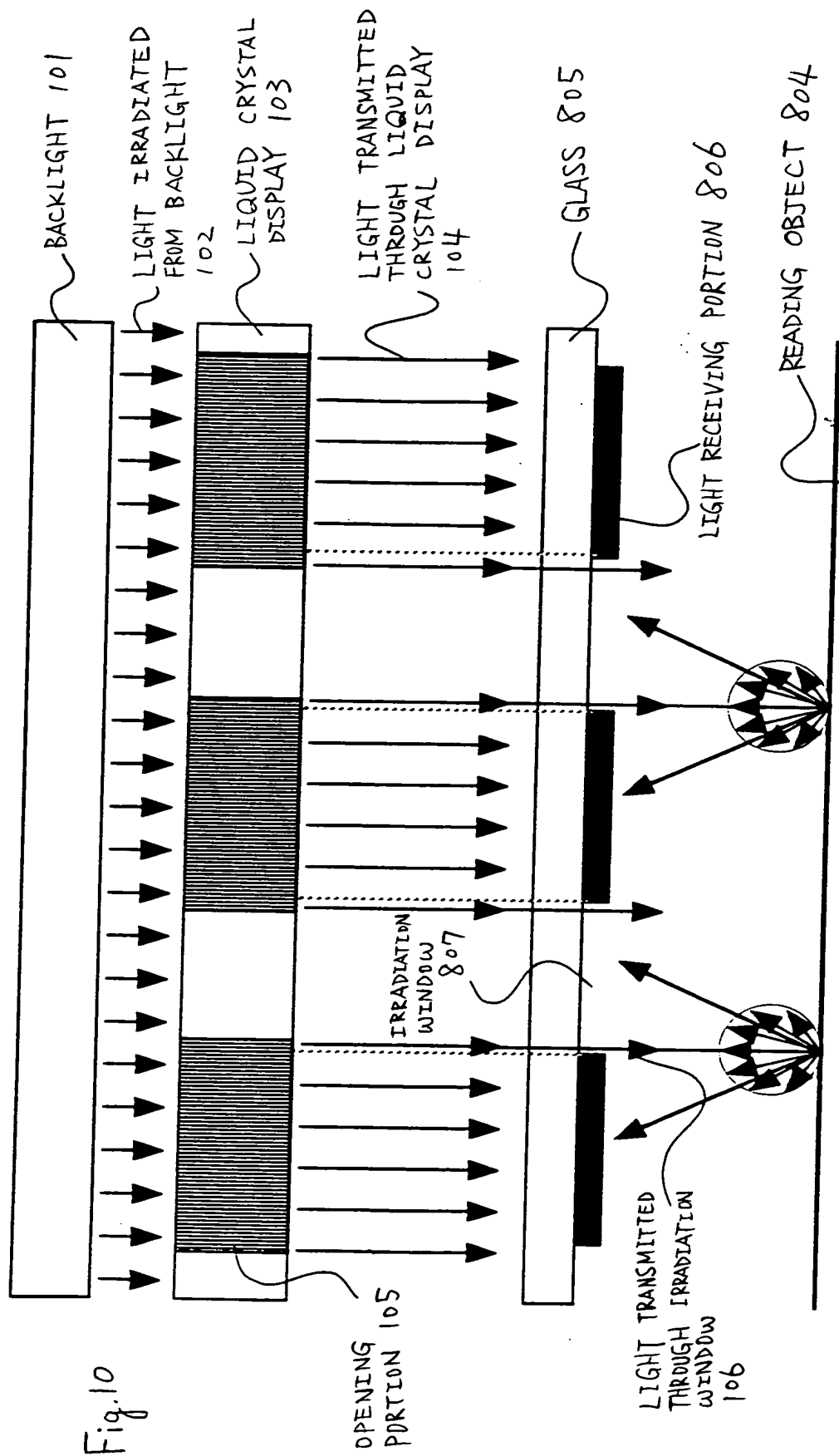


Fig. 10

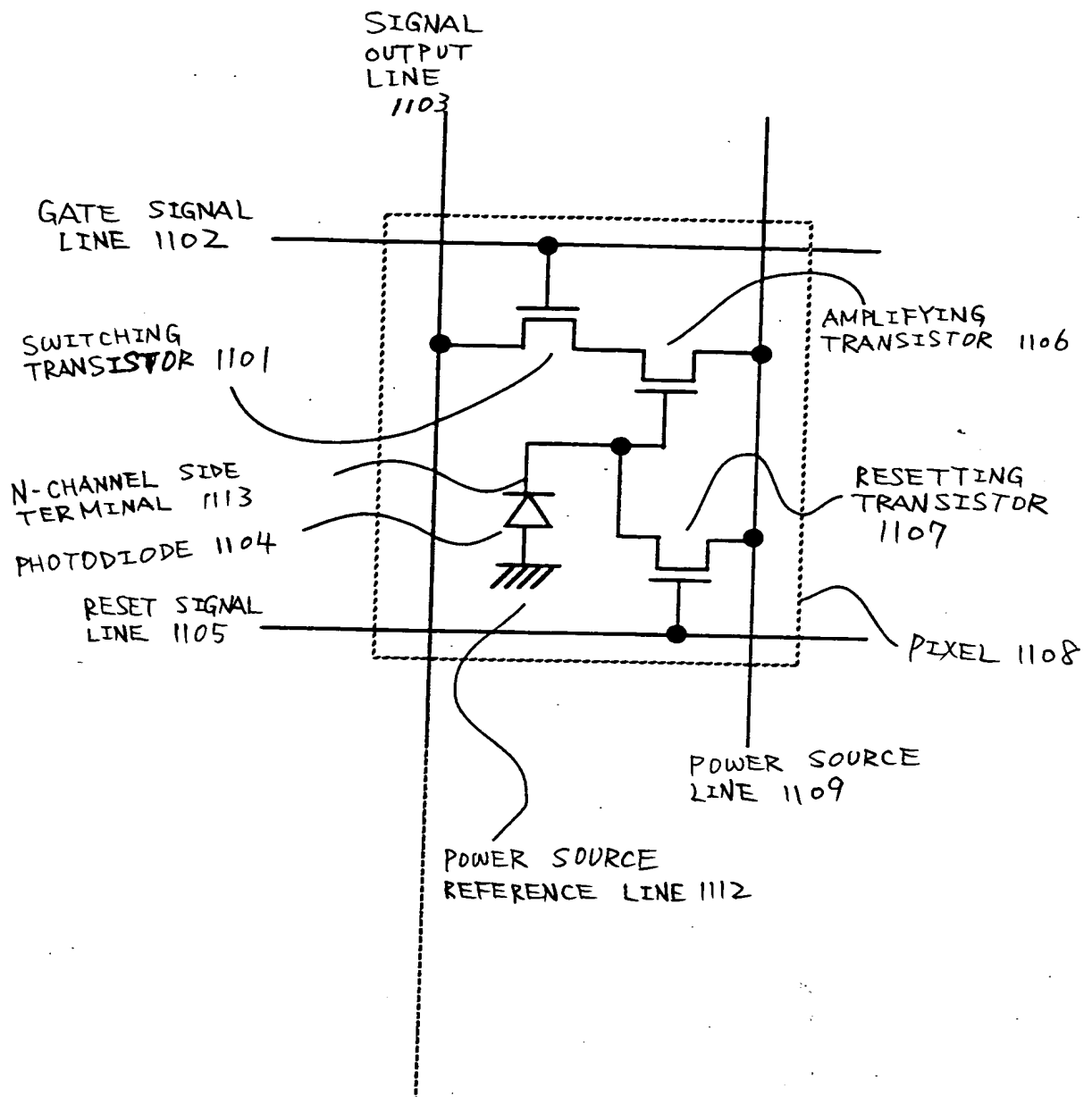
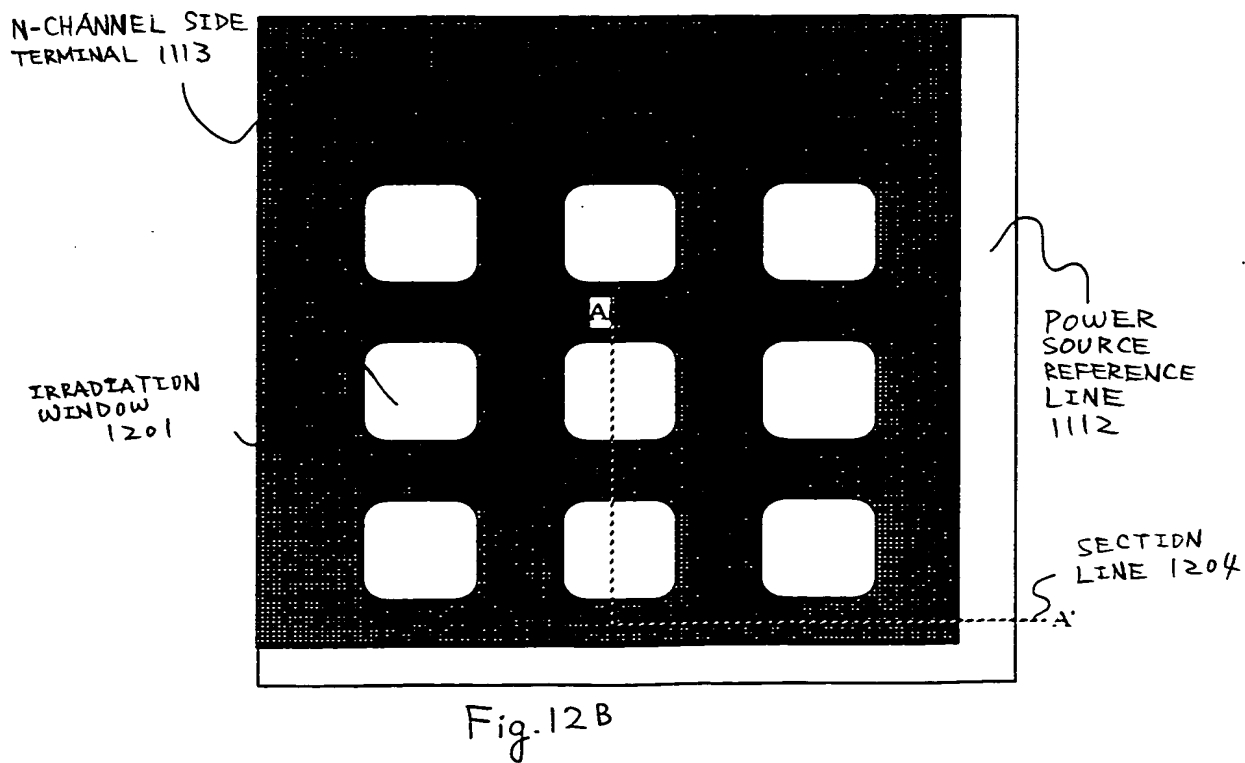
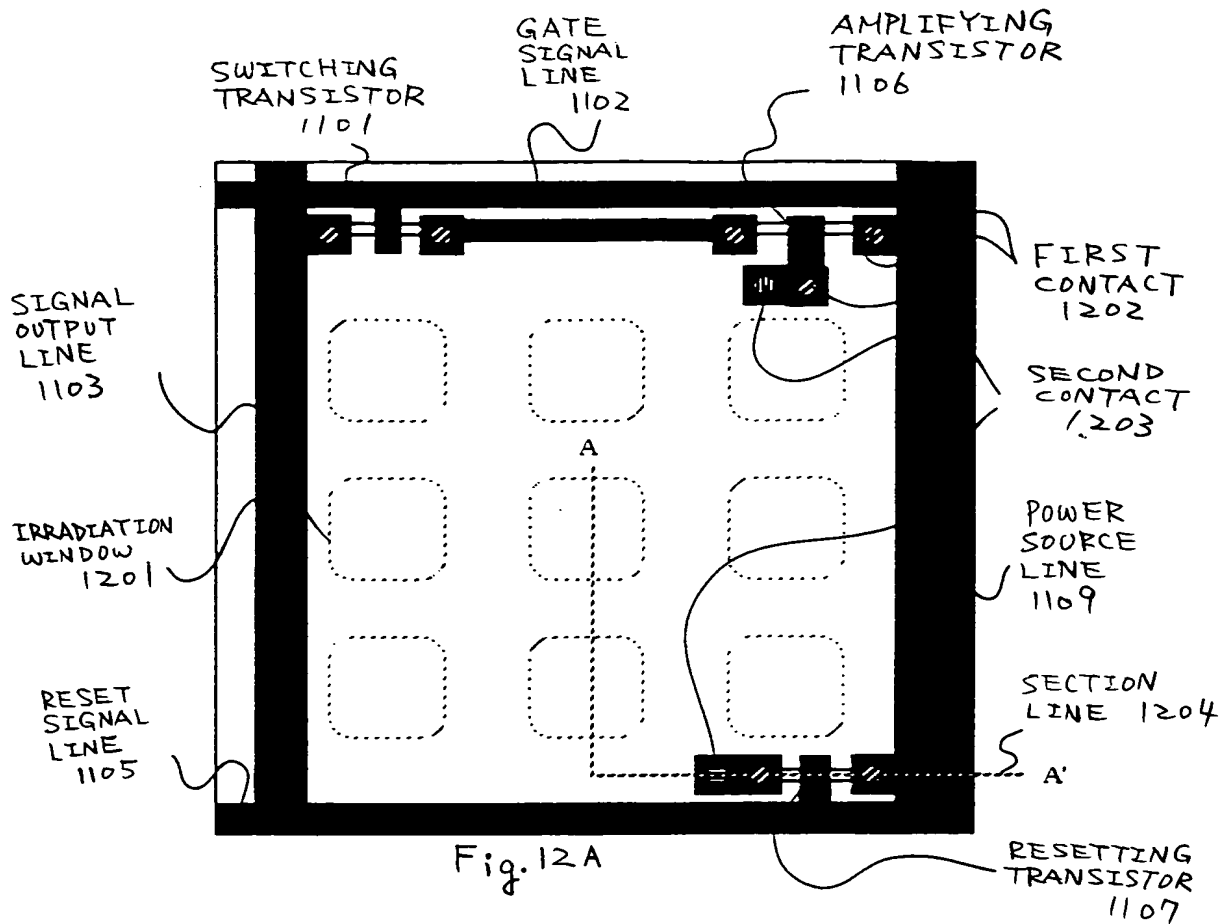


Fig. 11



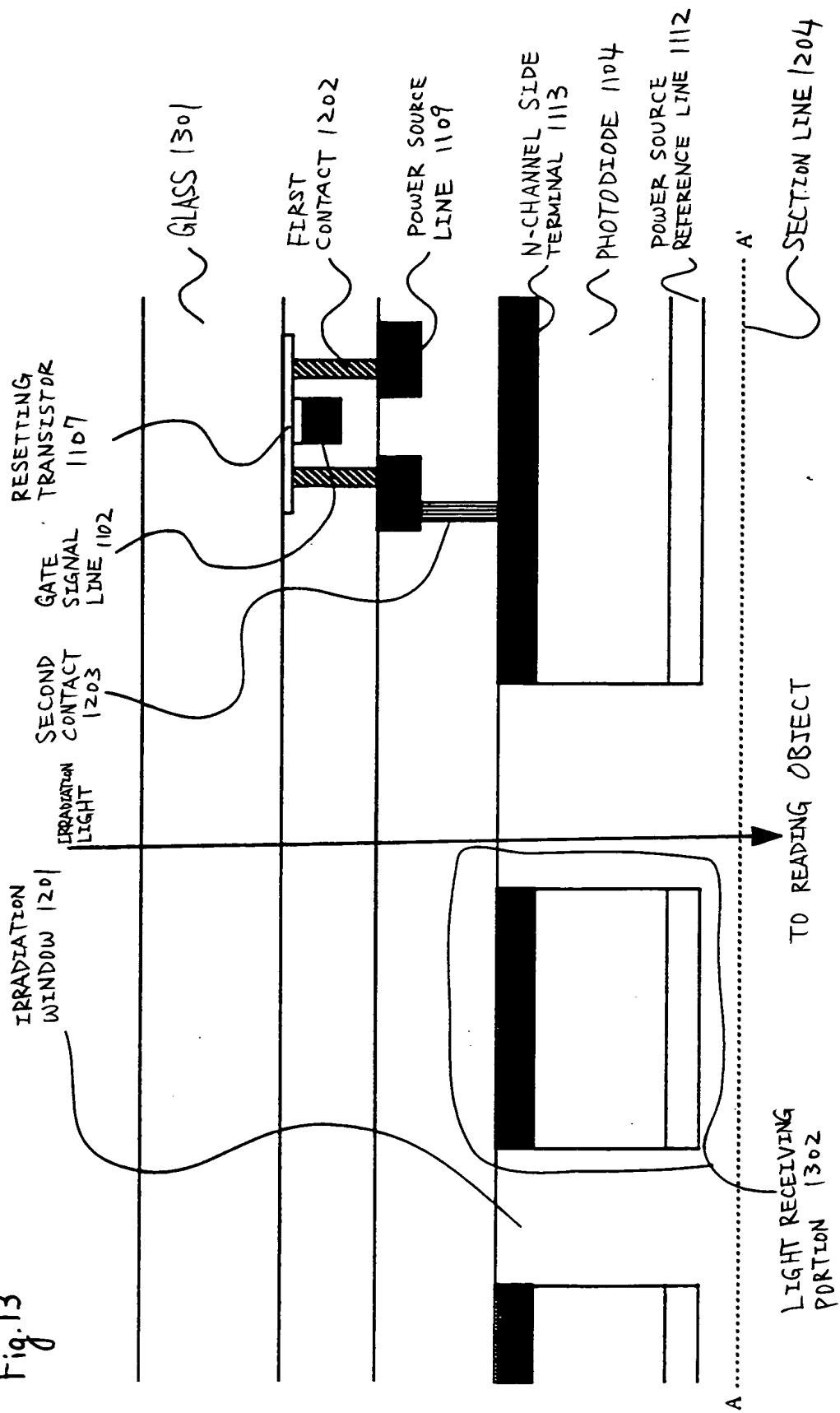
[illegible]

Fig. 14

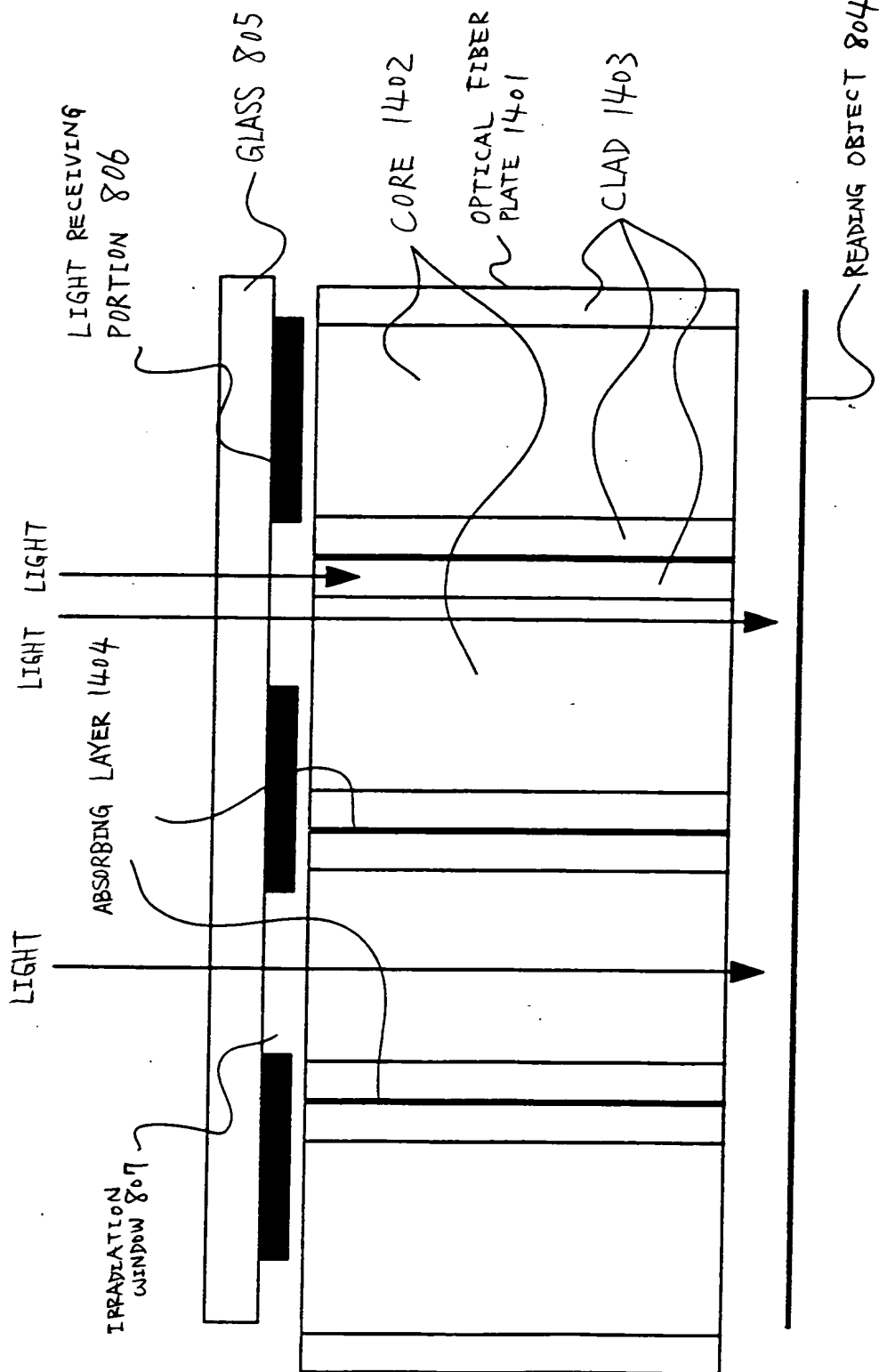


Fig. 15

UNIT PIXEL 1602 OF  
LIQUID CRYSTAL DISPLAY

LIQUID CRYSTAL DISPLAY  
1601

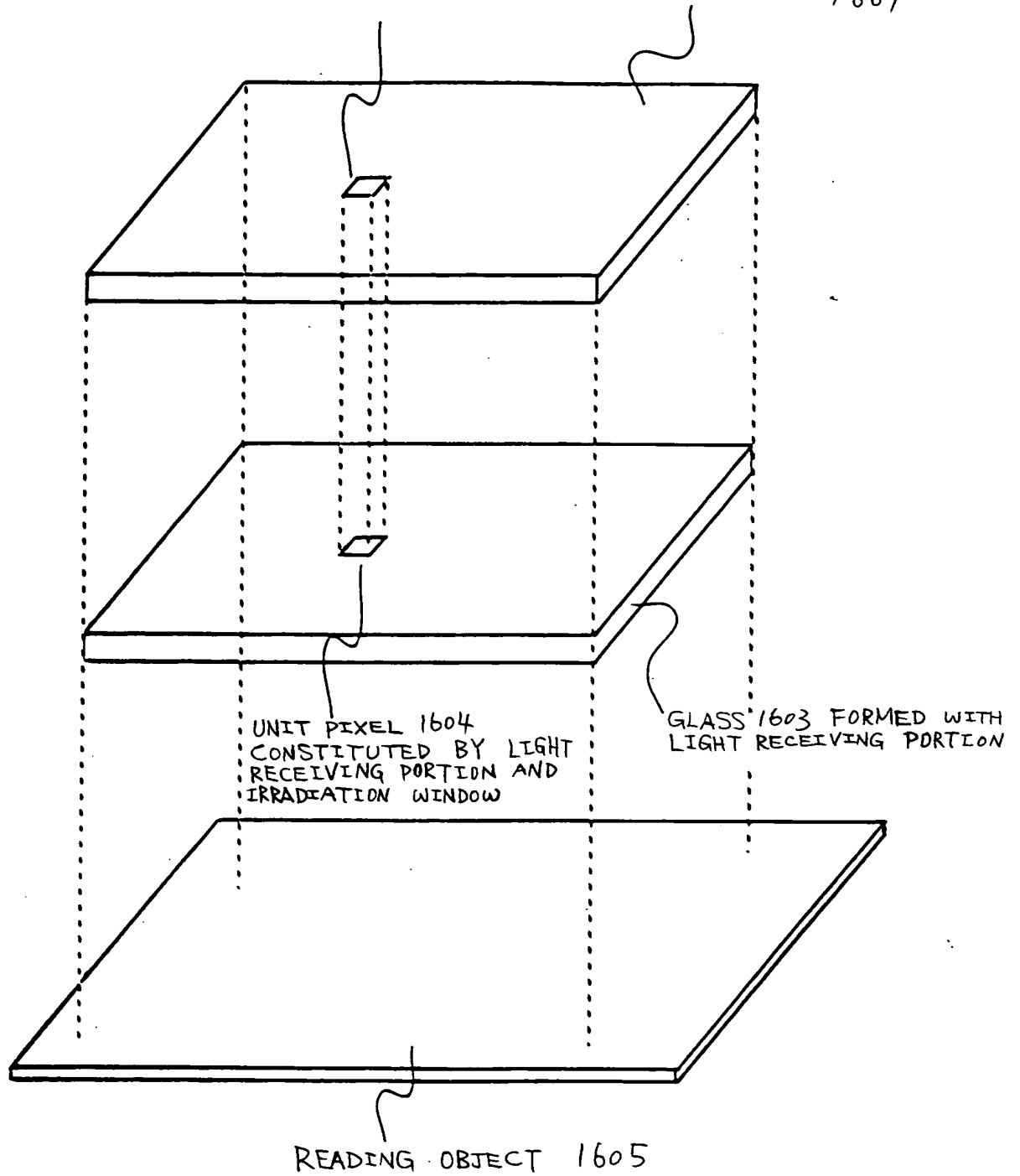


Fig. 16

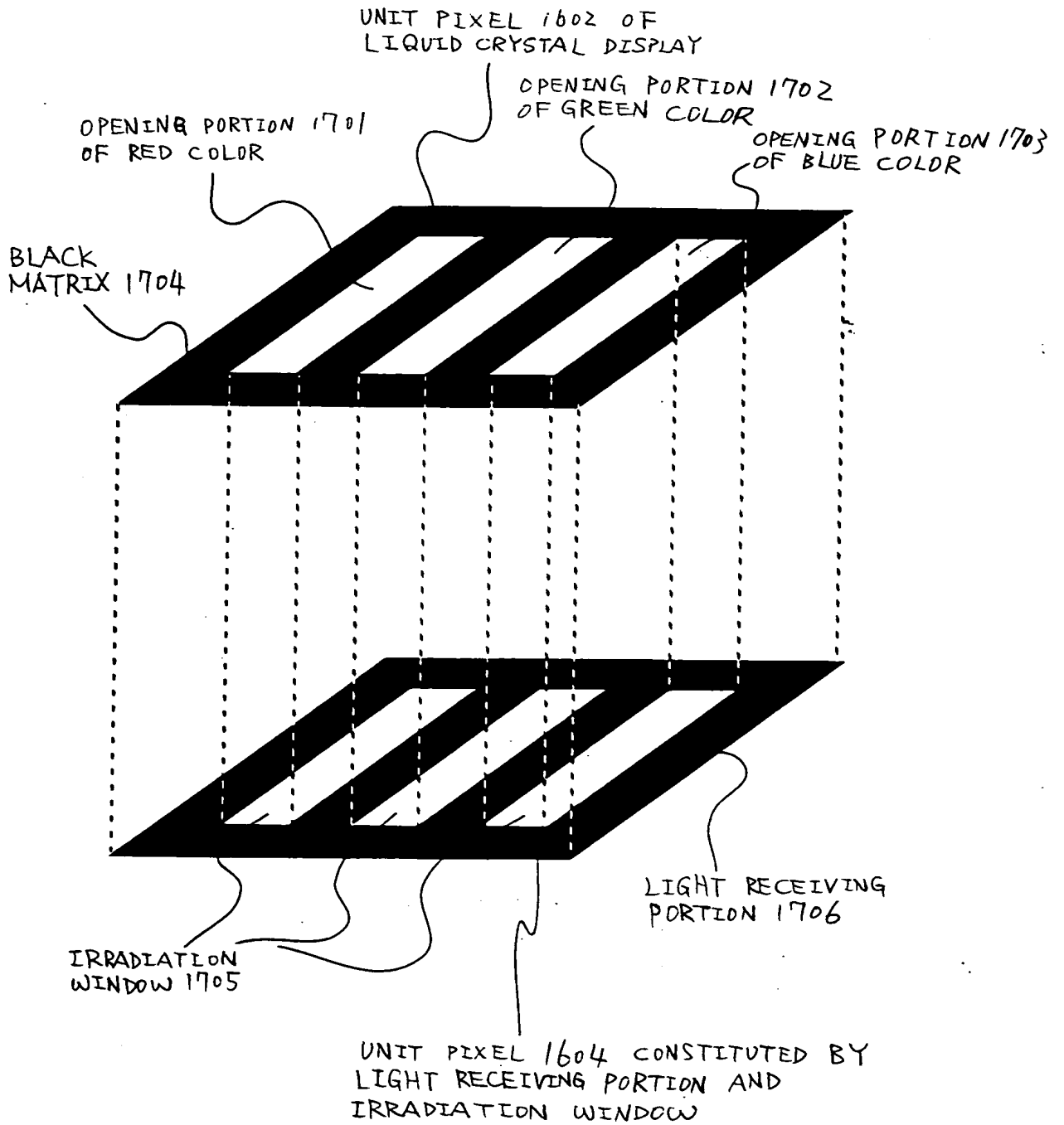


Fig. 17



$j$ -TH COLUMN  
PERIPHERAL PORTION  
CIRCUIT 1809

DRIVE CIRCUIT 1801 FOR  
SIGNAL OUTPUT LINE

CIRCUIT 1802 FOR SAMPLING AND  
HOLDING AND SIGNAL PROCESSING

BIASING CIRCUIT 1803

CIRCUIT 1804 FOR  
AMPLIFYING FINAL OUTPUT

Fig. 18

DRIVE CIRCUIT 1806  
FOR GATE SIGNAL  
LINE

DRIVE CIRCUIT 1807  
FOR RESET SIGNAL  
LINE

$i$ -TH ROW GATE SIGNAL  
LINE

PIXEL ARRANGEMENT  
PORTION 1805

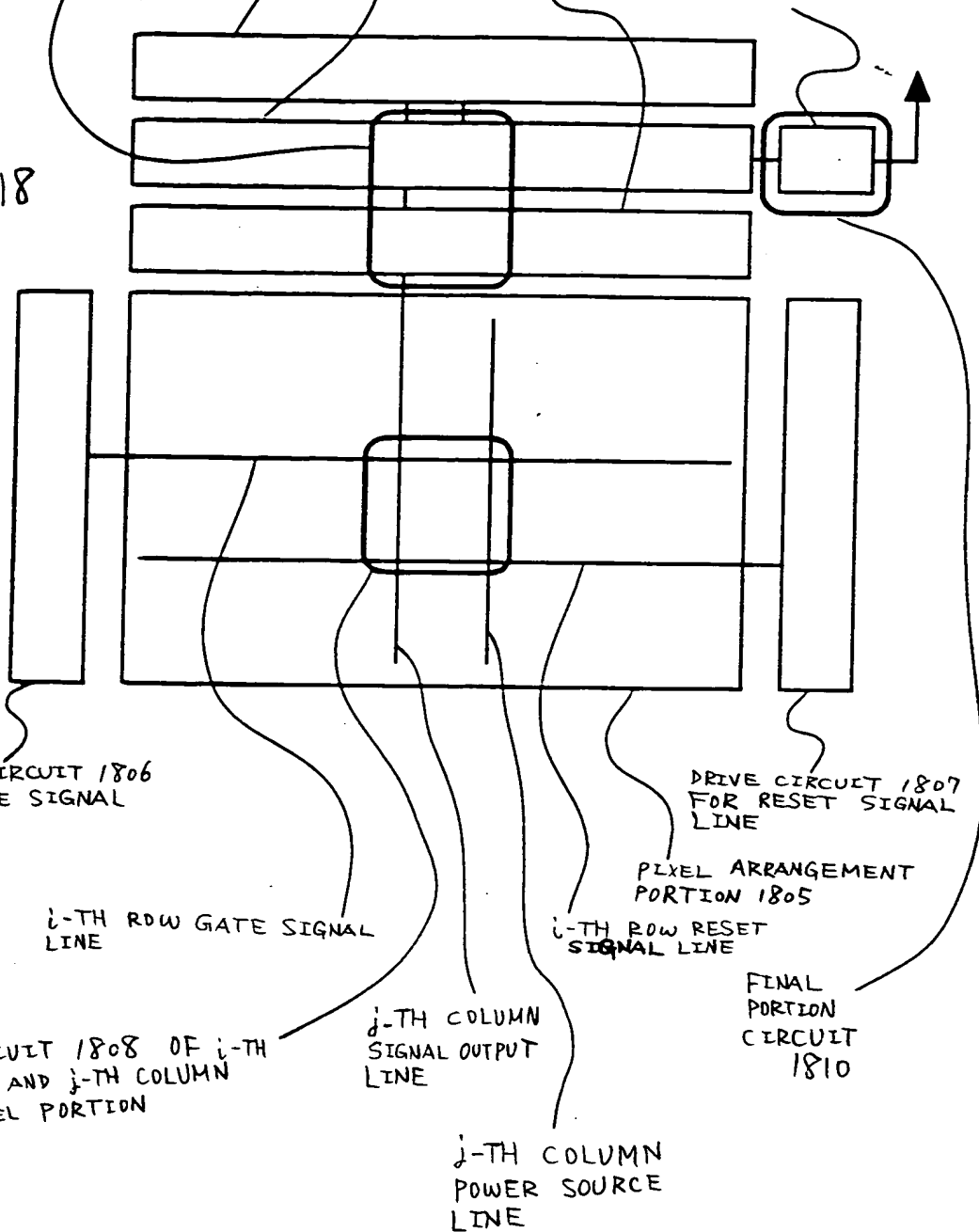
$i$ -TH ROW RESET  
SIGNAL LINE

CIRCUIT 1808 OF  $i$ -TH  
ROW AND  $j$ -TH COLUMN  
PIXEL PORTION

$j$ -TH COLUMN  
SIGNAL OUTPUT  
LINE

FINAL  
PORTION  
CIRCUIT  
1810

$j$ -TH COLUMN  
POWER SOURCE  
LINE



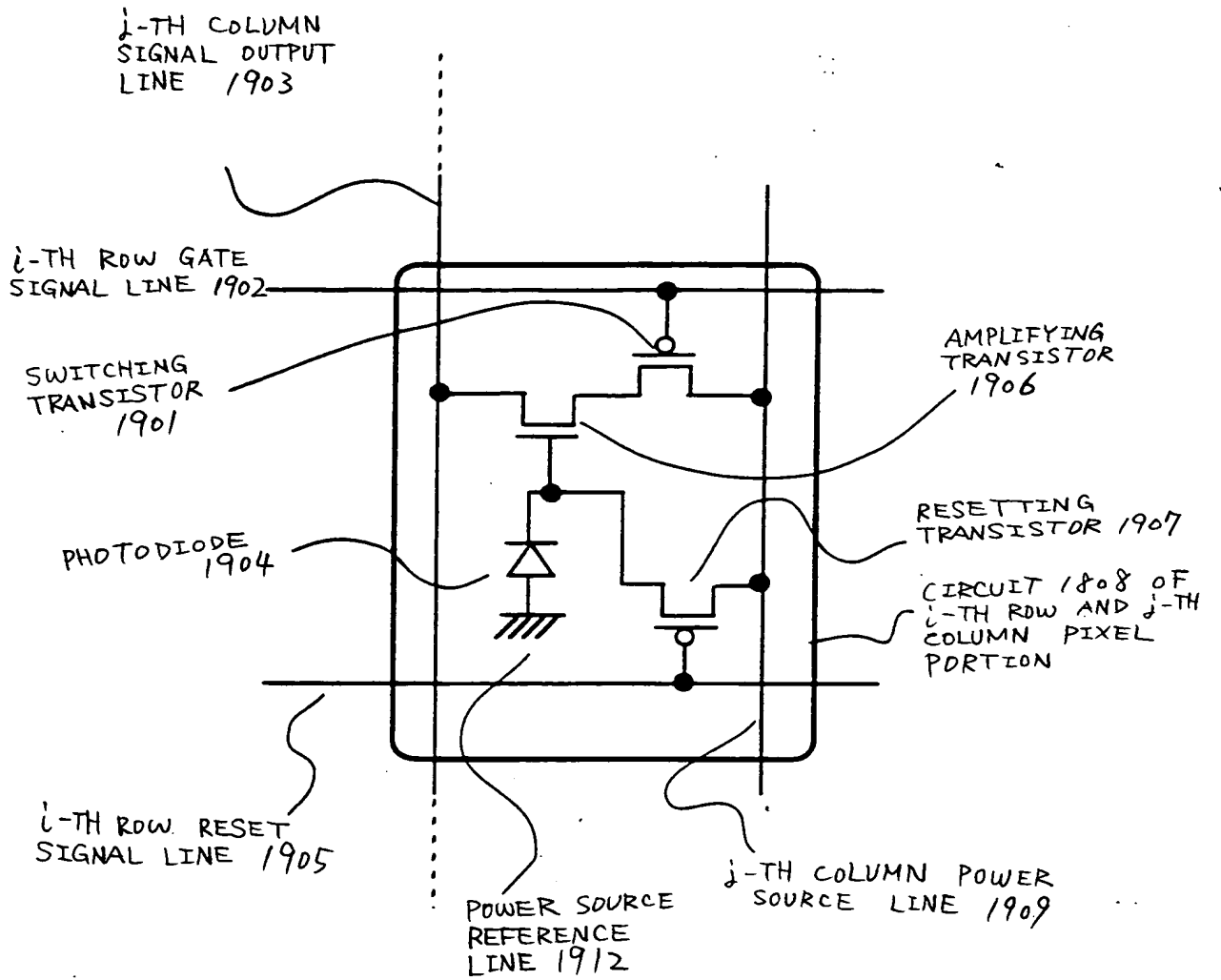


Fig. 19

$i$ -TH ROW SIGNAL  
OUTPUT LINE 2003

$i$ -TH ROW GATE  
SIGNAL LINE 2002

SWITCHING  
TRANSISTOR  
2001

PHOTODIODE  
2004

$i$ -TH ROW RESET  
SIGNAL LINE  
2005

AMPLIFYING  
TRANSISTOR  
2006

RESETTING  
TRANSISTOR  
2007

$j$ -TH COLUMN POWER  
SOURCE REFERENCE  
LINE 2012

POWER SOURCE  
LINE 2009

$i$ -TH ROW AND  $j$ -TH COLUMN  
PIXEL PORTION CIRCUIT 1808

BIAS SIGNAL  
LINE 2010

BIASING  
TRANSISTOR  
2011

Fig. 20

TOP SECRET

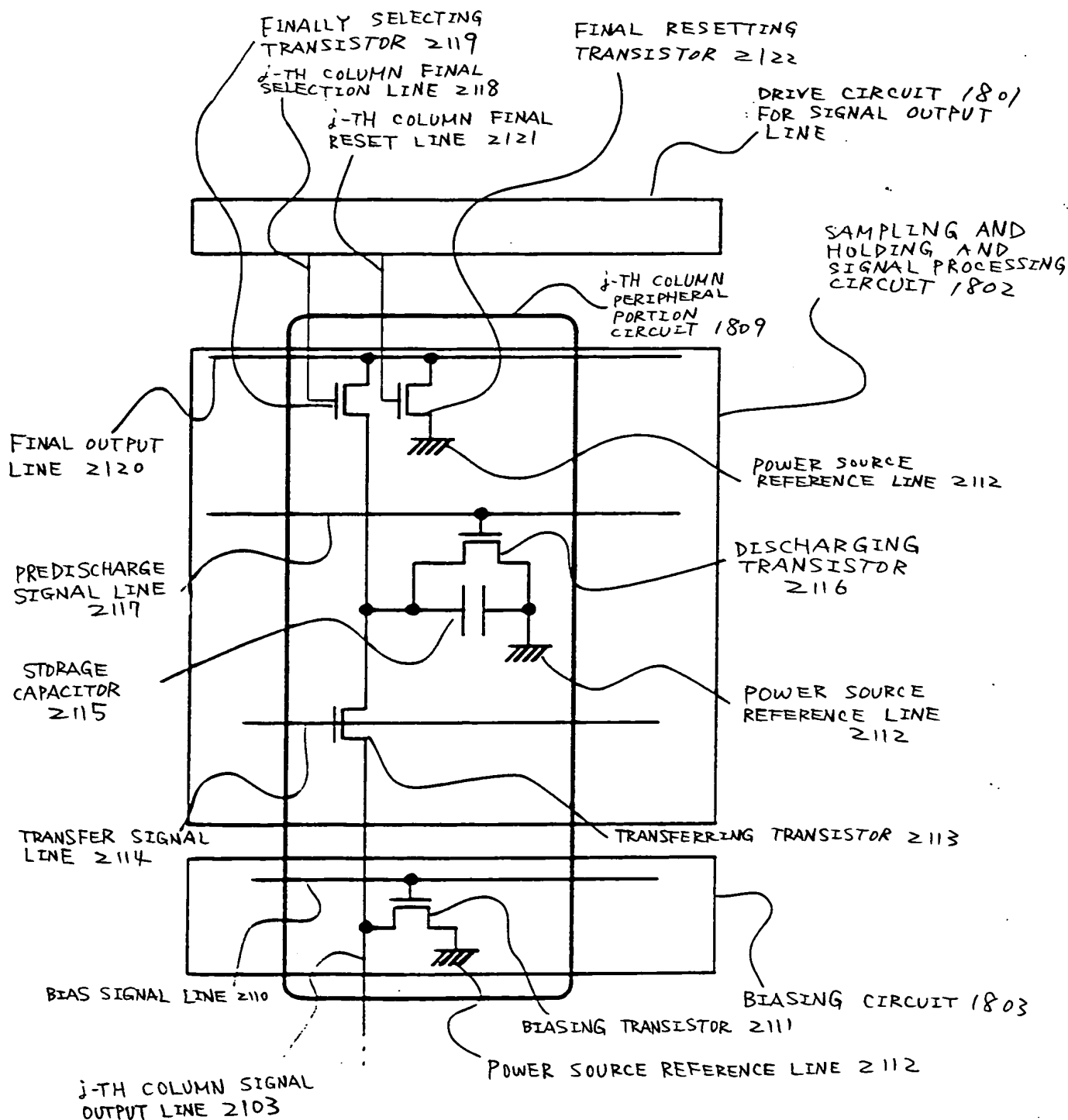


Fig. 21

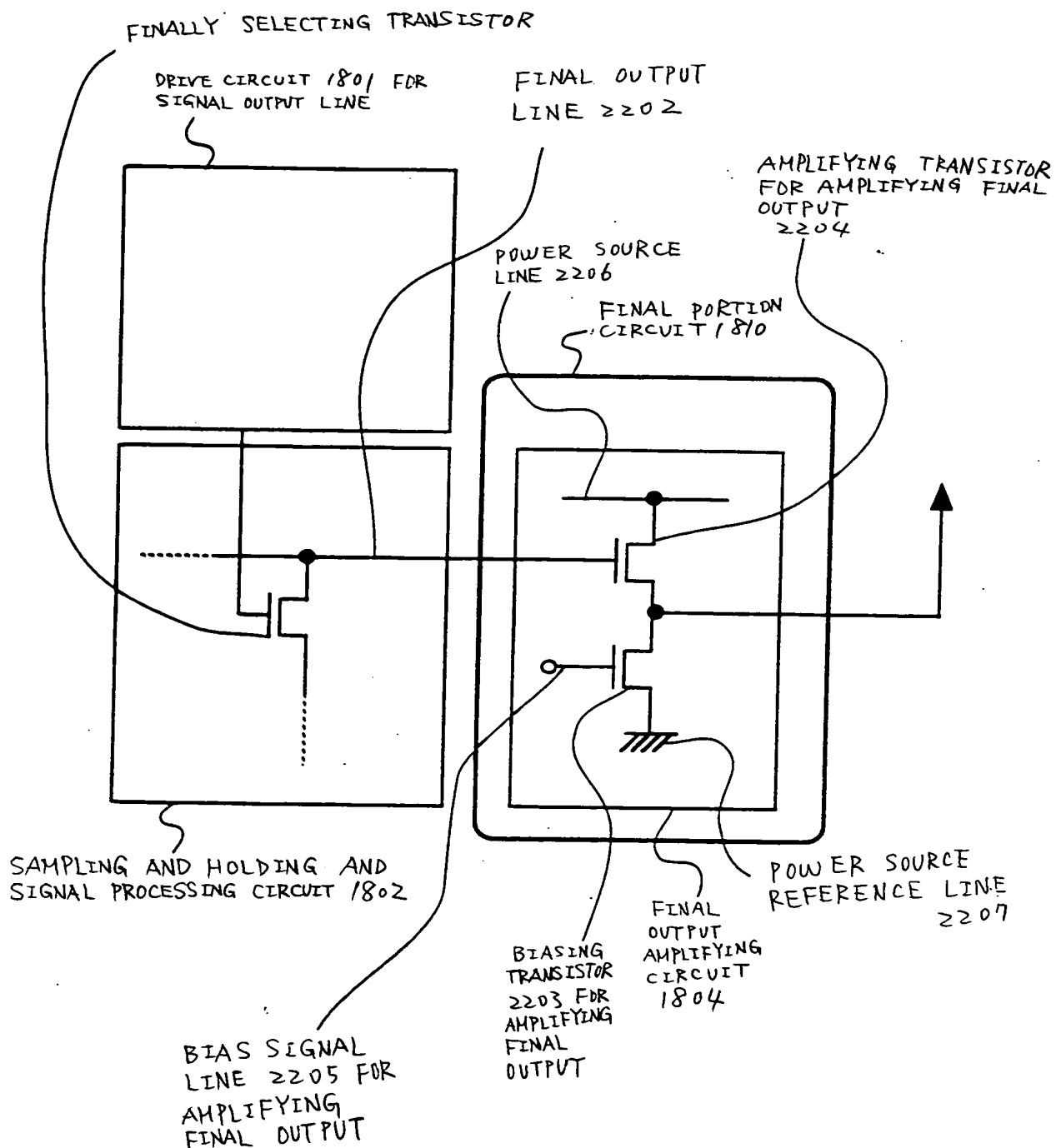


Fig. 22

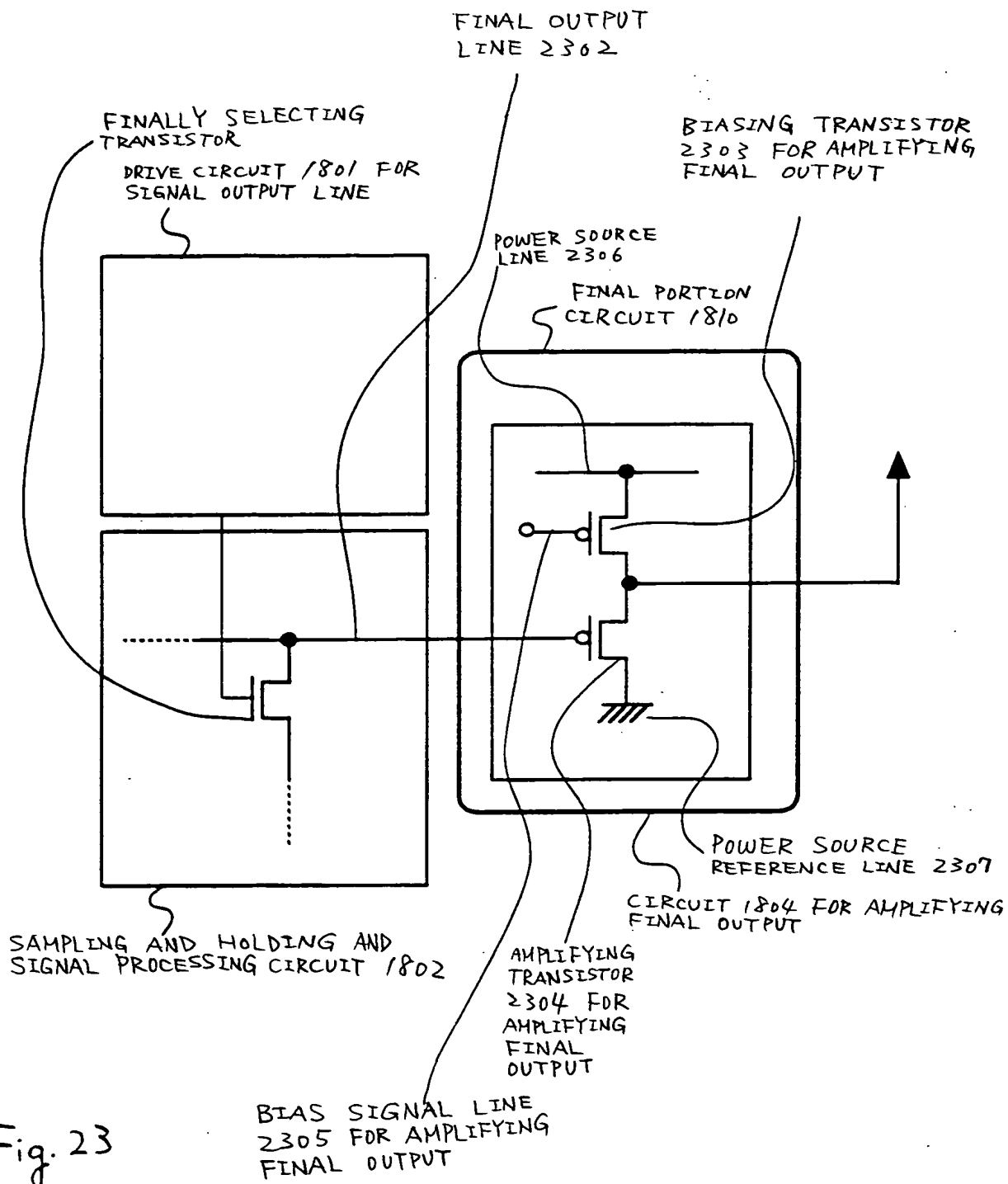


Fig. 24

RESET SIGNAL LINE OF  
(i-1)-TH ROW 1905

RESET SIGNAL LINE OF  
i-TH ROW 1905

RESET SIGNAL LINE OF  
(i+1)-TH ROW 1905

GATE SIGNAL LINE OF  
(i-1)-TH ROW 1902

GATE SIGNAL LINE OF  
i-TH ROW 1902

GATE SIGNAL LINE OF  
(i+1)-TH ROW 1902

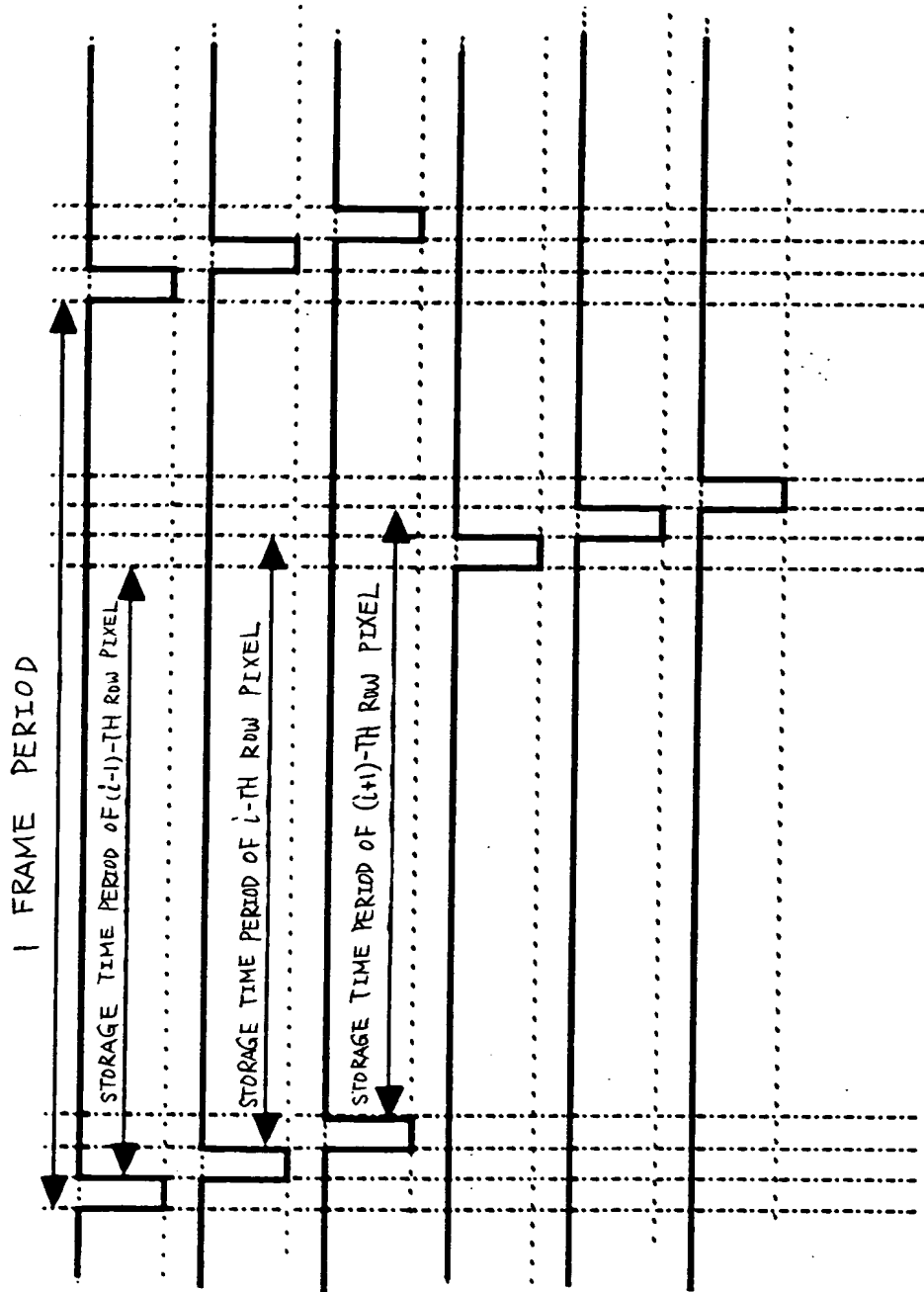
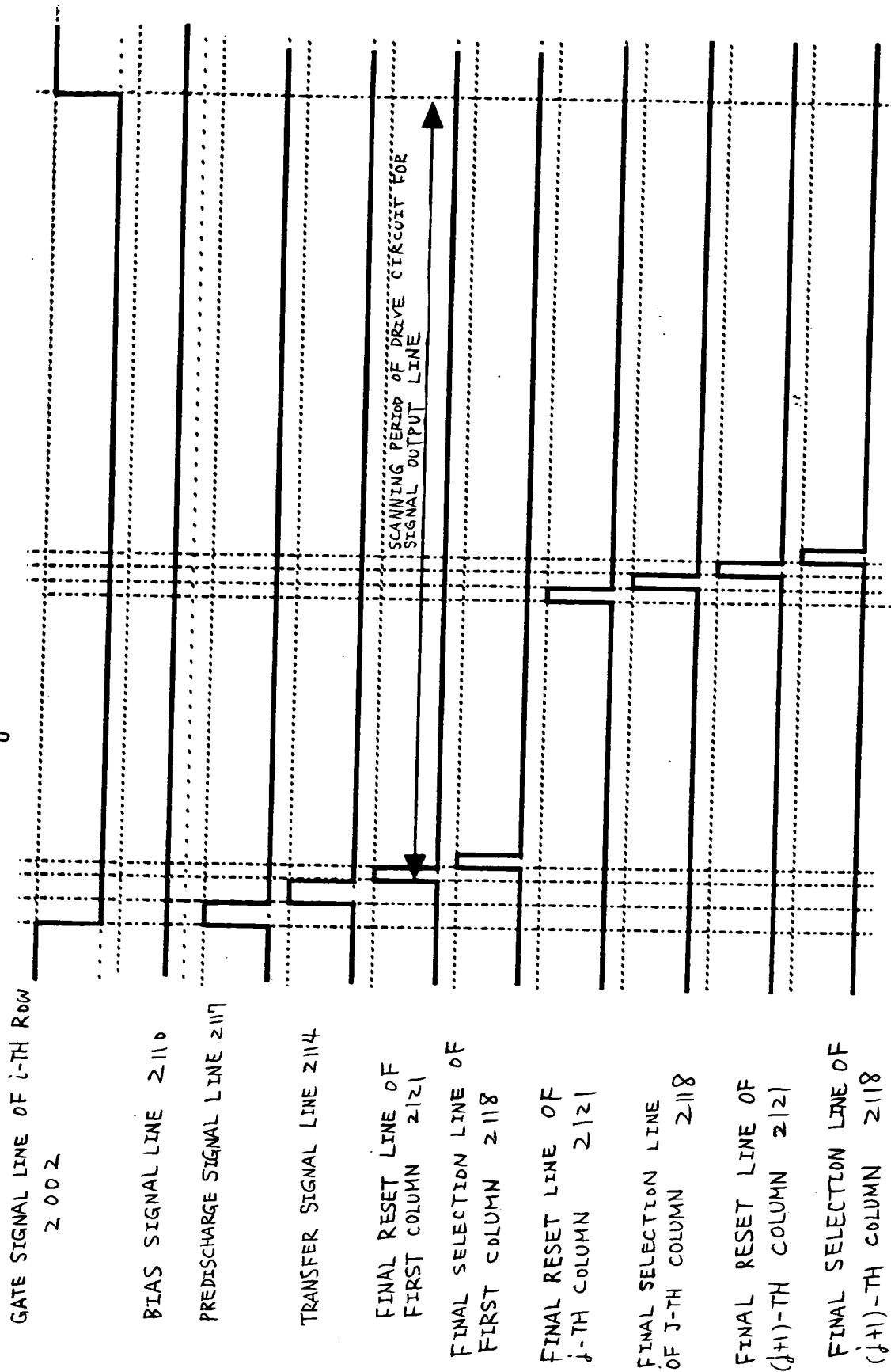


Fig. 25





CRYSTALLIZATION STEP

Fig. 26A

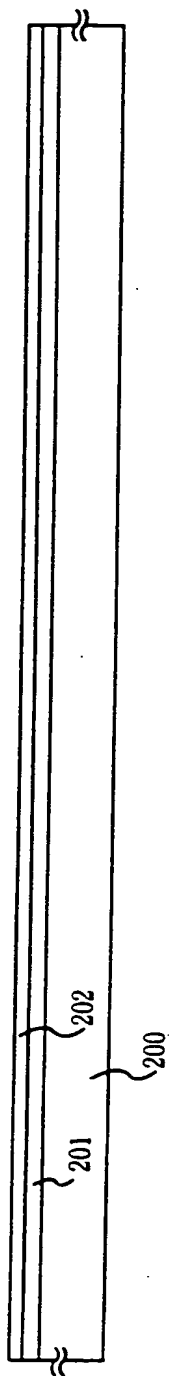
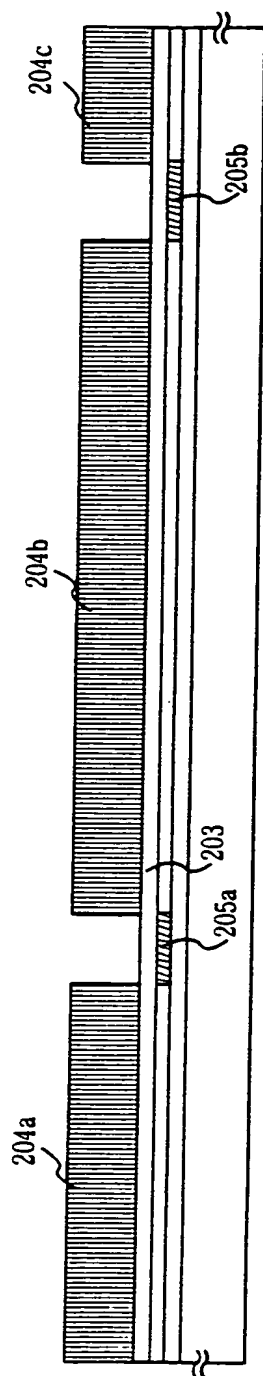


Fig. 26B



LASER ANNEALING STEP

Fig. 26C

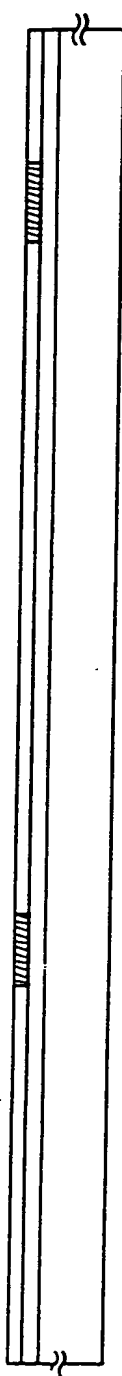


Fig. 26D

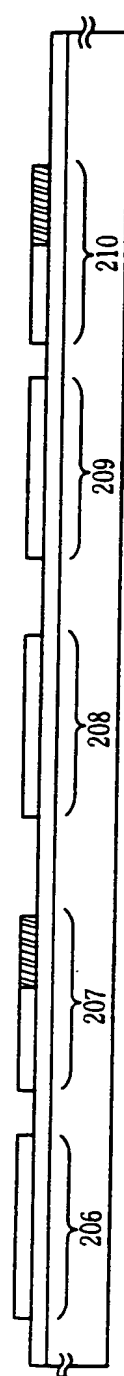


Fig. 27A

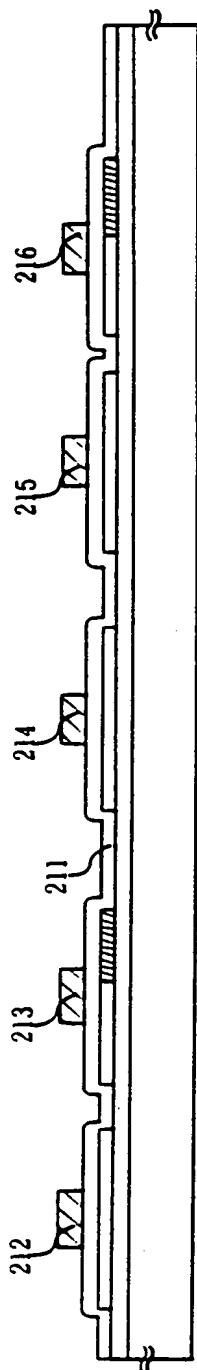


Fig. 27B

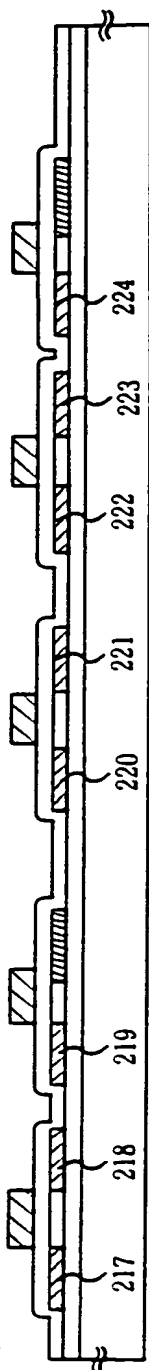


Fig. 27C

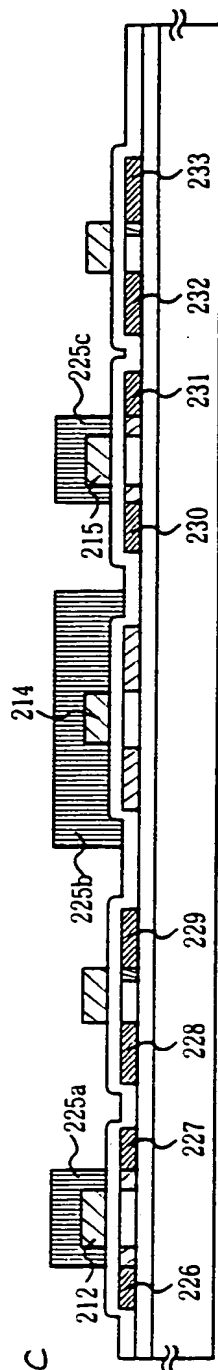


Fig. 27D

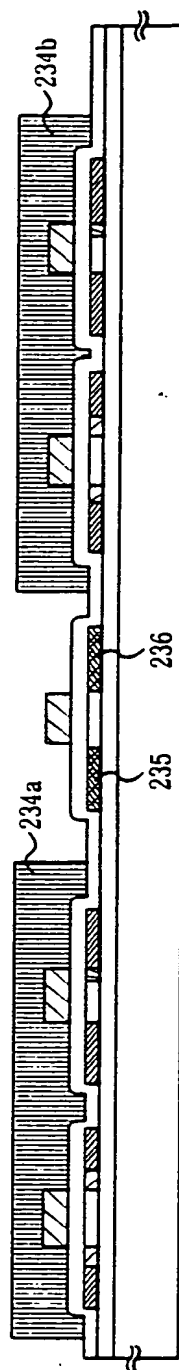


Fig. 28A

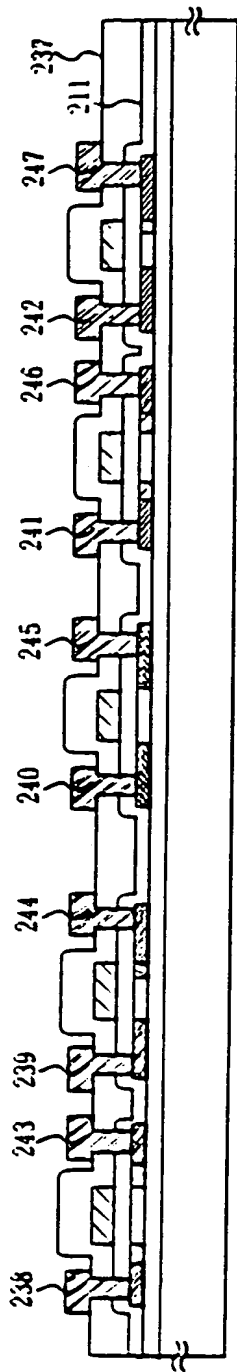


Fig. 28B

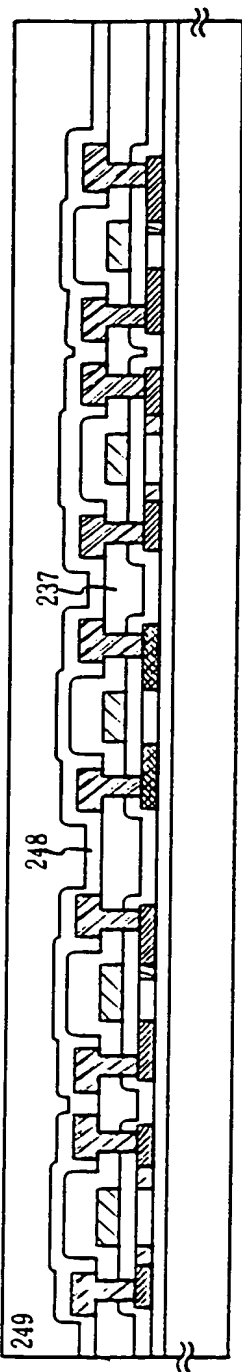


Fig. 28c

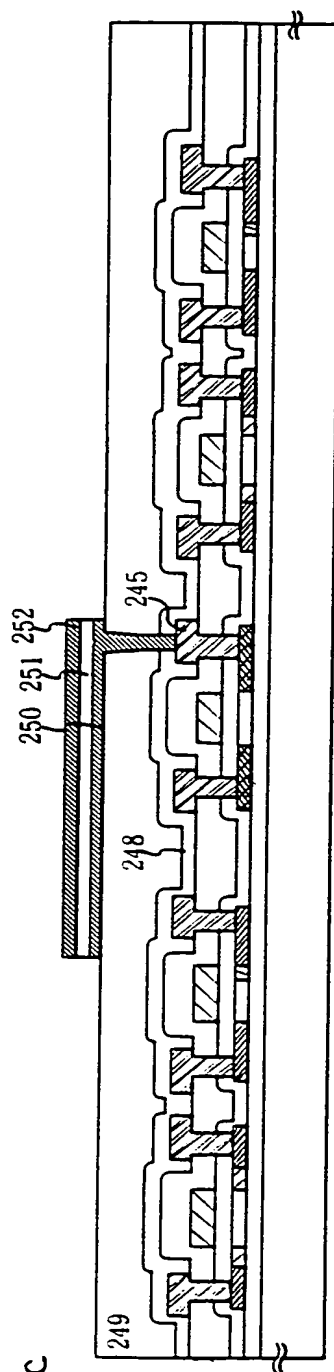


Fig. 29A

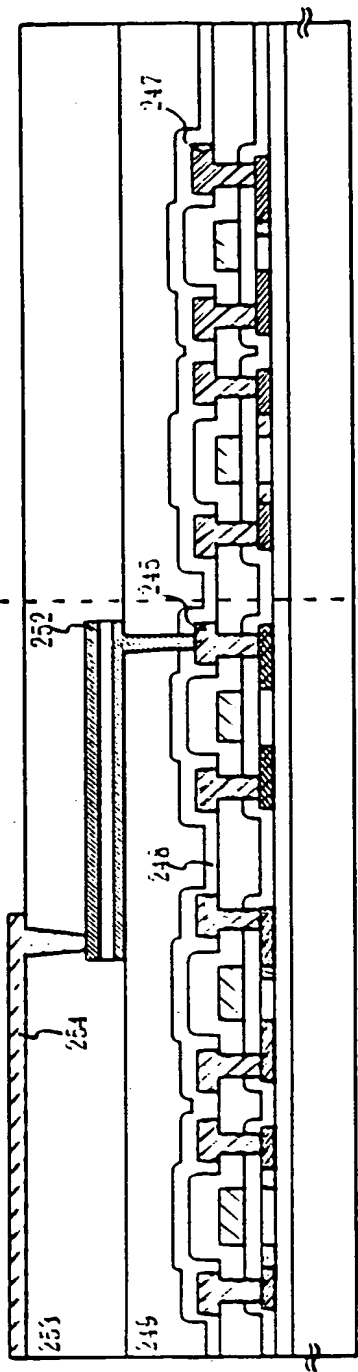
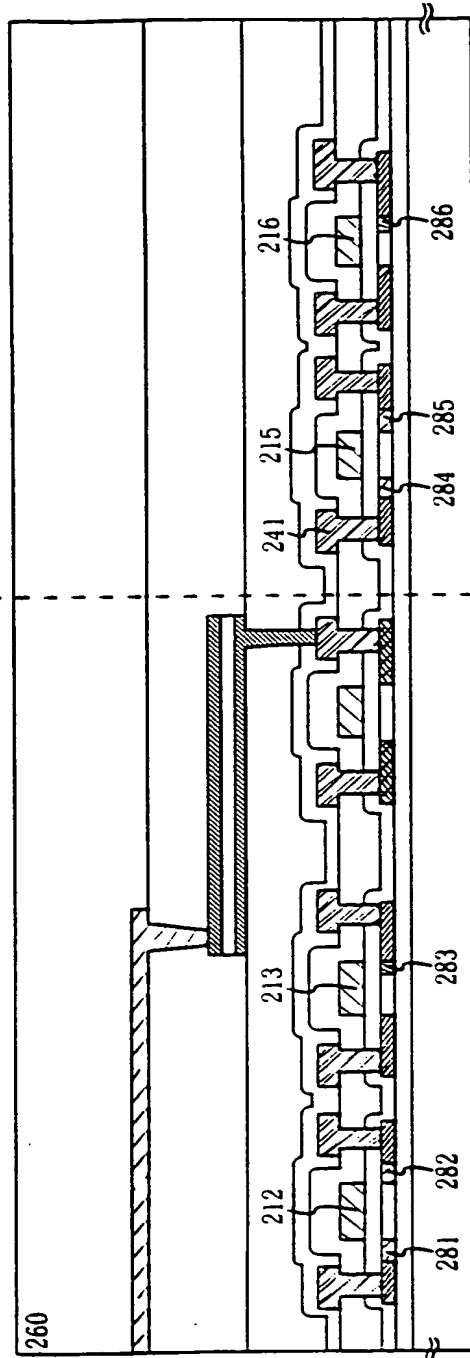


Fig. 29B



AMPLIFYING TFT 270 SWITCHING TFT 271 RESETTING TFT 272 BIASING TFT 273 DISCHARGE TFT 274

Fig. 30A

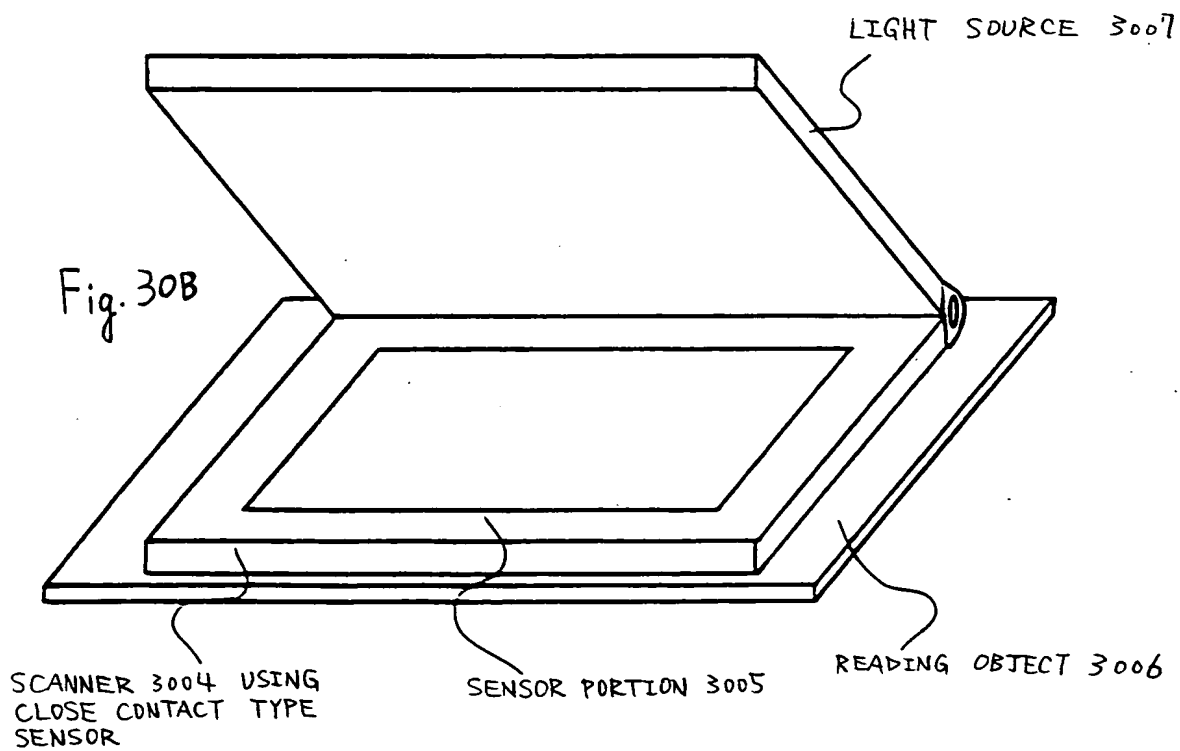
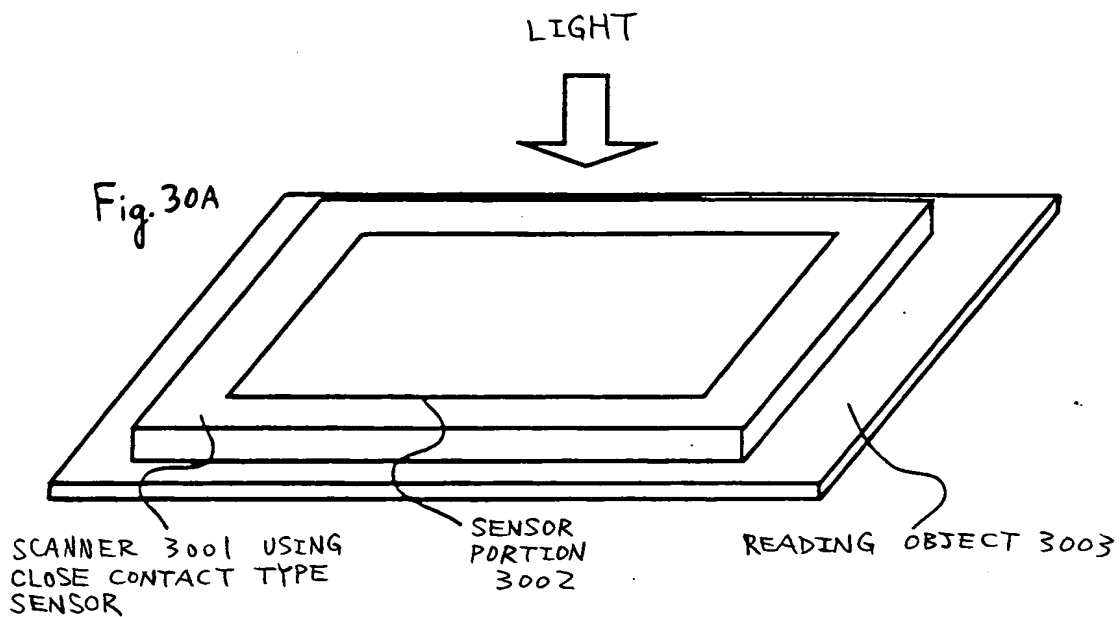


FIG. 31A

